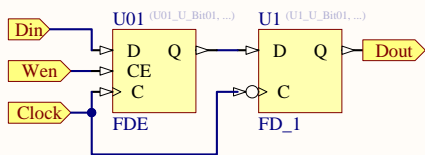


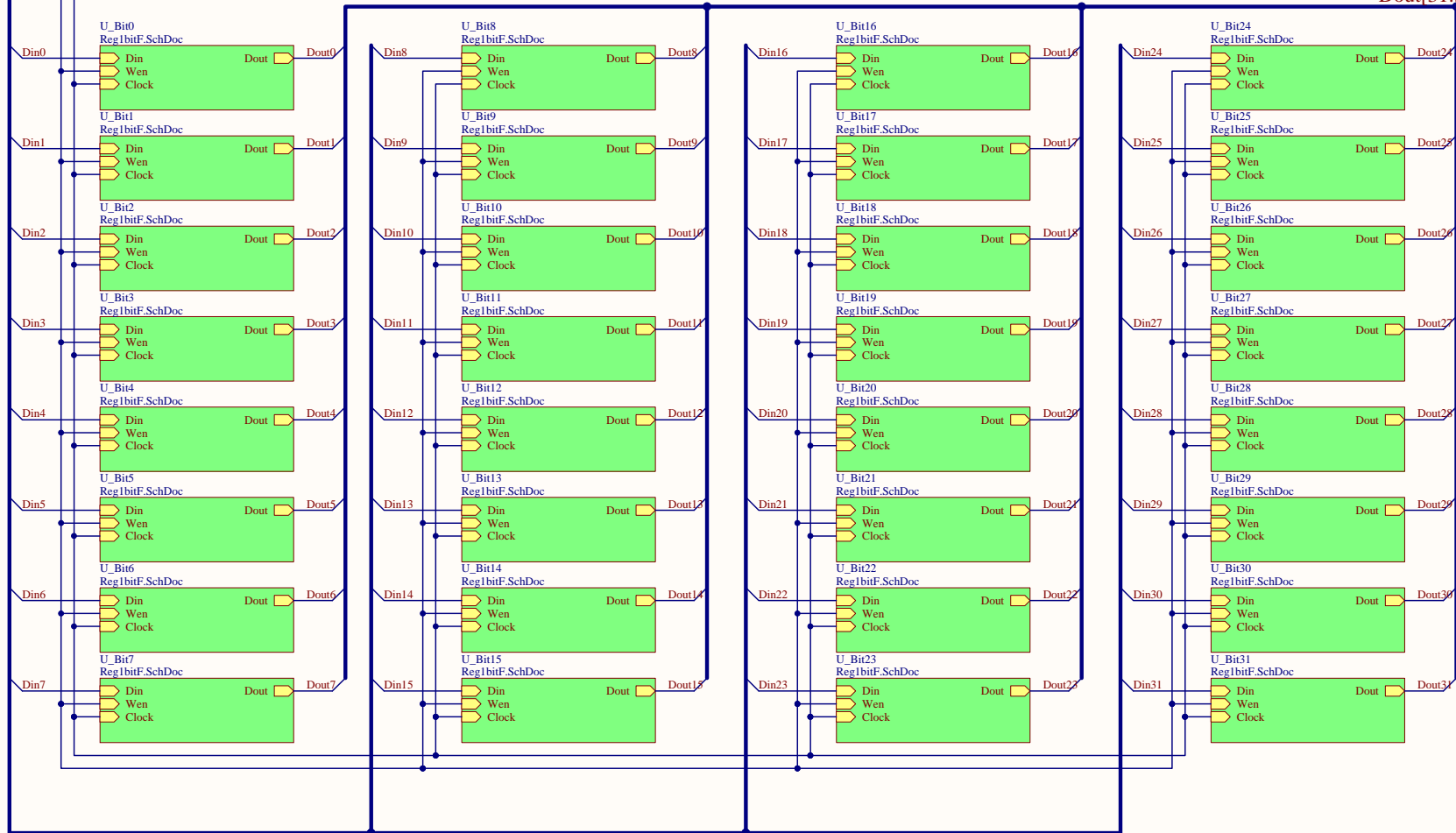
1-bit Register, Write enable,
falling edge triggered



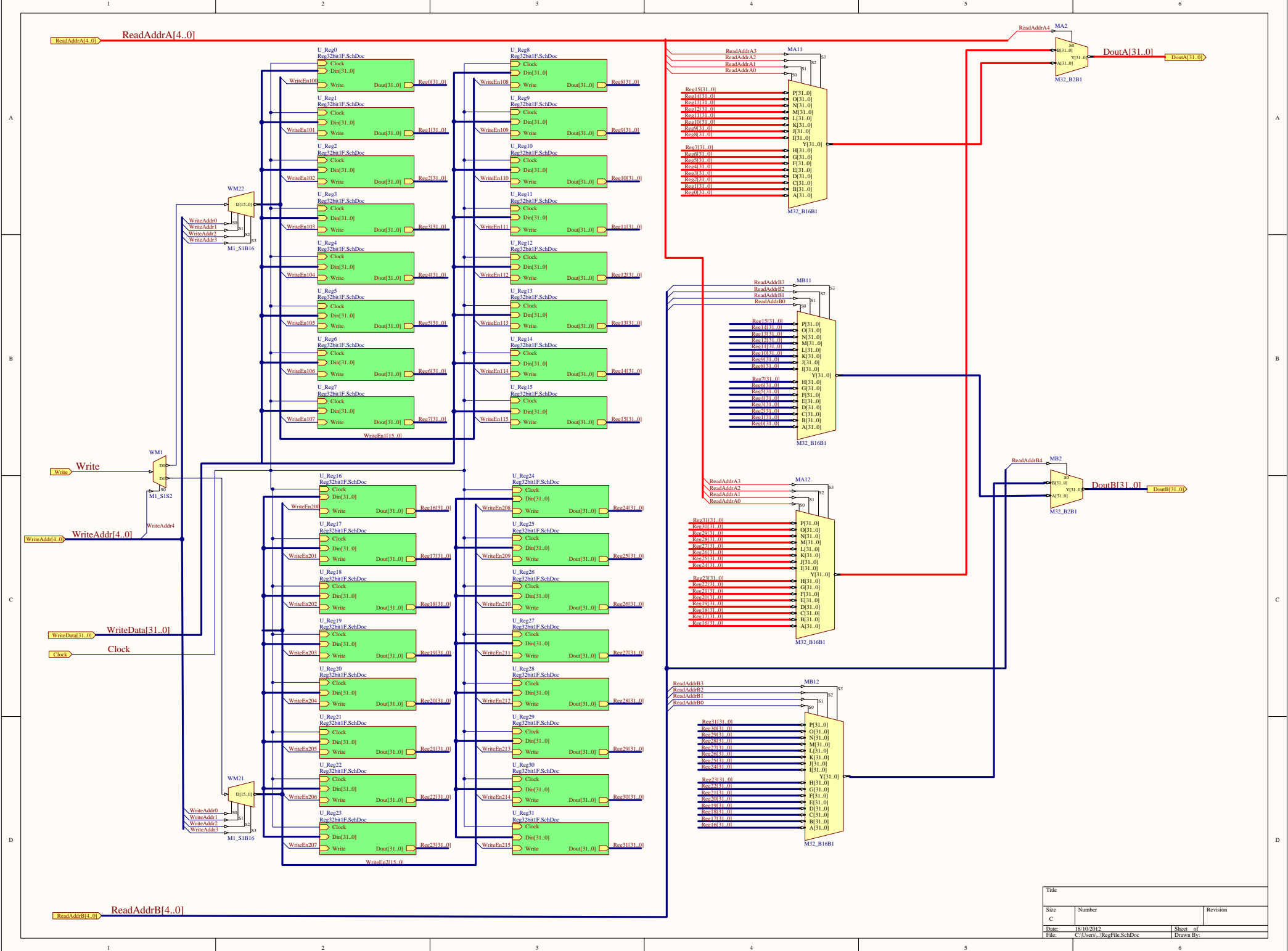
Title		
Size	Number	Revision
A		
Date:	18/10/2012	Sheet of
File:	C:\Users\...\Reg1bitF.SchDoc	Drawn By:

Clock
Write
Din[31..0]

Dout[31..0]



Title		
Size	Number	Revision
B		
Date:	18/10/2012	Sheet of
File:	C:\Users\..._Reg32bit1F.SchDoc	Drawn By:



Title		
Size	Number	Revision
C		
Date:	18/10/2012	Sheet of
File:	C:\Users\..._RegFile_SchDoc	Drawn By: