SC/CSE 3213 Winter 2014

L12: Basic Baseband Digital Comms



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Outline

- · Basic digital communicator blocks
- Nyquist pulses
- Channel capacity

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Underlying Idea

Attempting to send a sequence of digits through a continuous channel

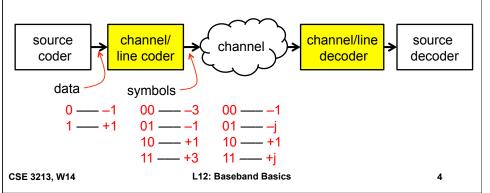


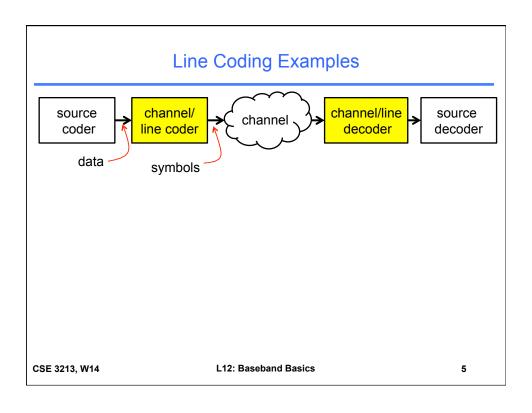
- · Not easy...
- · ...modulerize the design

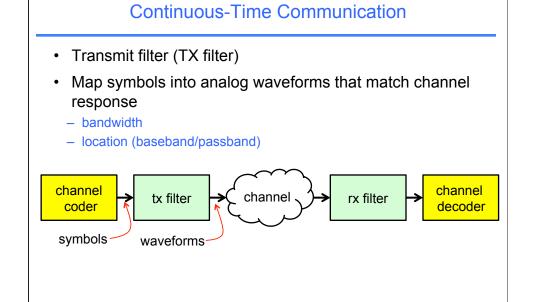
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The Information Theory Perspective

- Exchanging sequences of discrete symbols
- · source coding
 - increase redundancy
- · channel coding
 - error correction (add redundancy)
- line coding
 - map bits to symbols





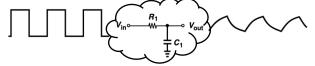


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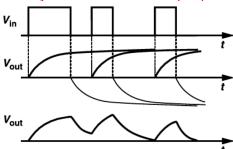
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Baseband Channel Problems

• Appreciate need to design your waveforms to channel



 Prolonged rise and fall times corrupt energies of adjacent symbols: intersymbol interference (ISI)

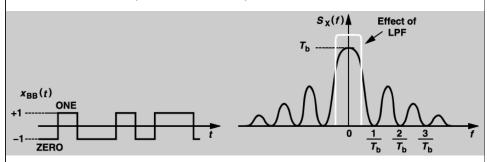


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A Spectral Perspective

• Random bit patterns sinc² spectrum



- Need extremely wide LPF to pass this without distortion
- Otherwise deal with ISI (intersymbol interference)

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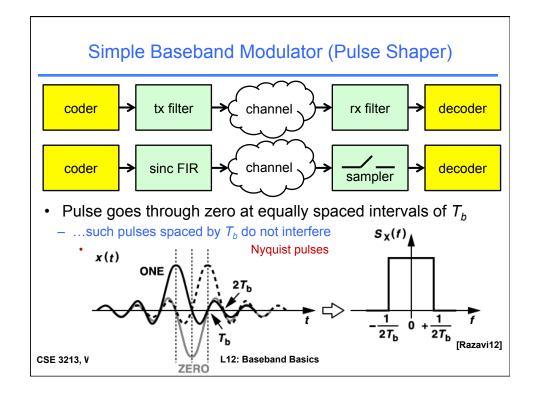
Pulse Shaping, Sinc Pulses

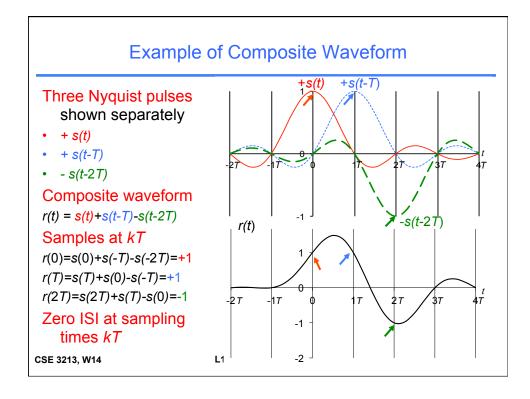
- · What is the ideal bandlimited pulse shape for the tx filter to produce?
- A sinc pulse... $S_X(f)$ x(t) $-T_b$ 0 $+T_b$
 - ...has spectrum that is neatly confined (to $\pm 1/2T_b$)
 - And there's another important advantage of this...

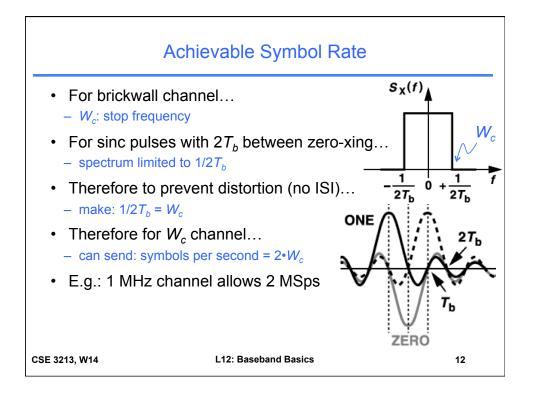
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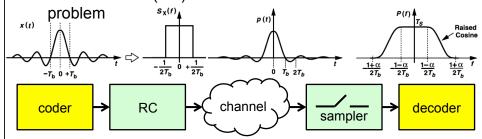






Raised Cosine (RC)

- Simple sinc has a lot of temporal energy away from main pulse
 - Sampling error can lead to lots of corruption
- Raised cosine (RC) waveform trades bandwidth for this



- excess-bandwidth/roll-off factor, α
 - **10-20%**

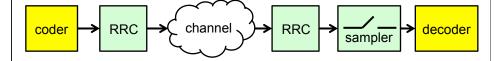
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Root Raised Cosine (RRC)

- · Very important for receiver to filter-out noise
 - minimize corruption to sampler
- Split up RC for simultaneous noise filtering and pulse shaping

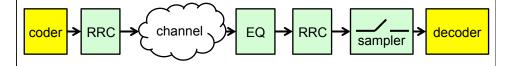


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Equalization & Matched Filtering

- · What if channel not a perfect brickwall or RC filter?
 - our previous assumptions would not match such a general channel and hence be distorted
- Compensate for channel with another filter: equalizer (EQ)



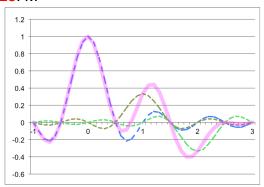
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Achievable Bit Rate, R

- Account for alphabet size: M
 - binary, M = 2
 - multilevel M > 2
 - 4-level: 00 --- -1 01 --- -1/3
 - 10 +1/3 11 — +1
- $R = 2W_c \cdot \log_2(M)$
- For example:
 - $-W_c = 1 \text{ MHz}$
 - M = 4
 - -R = 4 Mbps (2 MSps)



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Spectral Efficiency

· What is the bit-rate relative to the spectrum used:

$$v = \frac{\text{bit-rate}}{\text{bandwidth}}$$

$$v = \frac{2W_c \cdot \log_2(M)}{W_c} \left[\frac{\text{bits}}{\text{s} \cdot \text{Hz}} \right]$$

- Simple 2-level baseband modulation 2 bits/s•Hz
- or 2 symbols/s•Hz

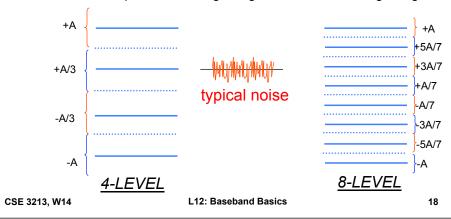
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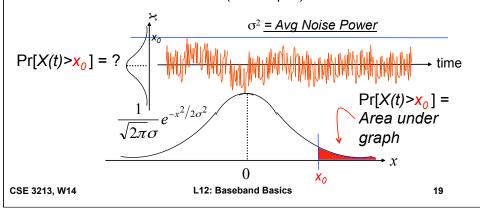
Noise Limits Accuracy

- Receiver makes decision based on transmitted pulse level + noise
- · Error rate depends on relative value of noise and level spacing
- Large (positive or negative) noise values can cause wrong decision
- Noise level impacts 8-level signaling more than 4-level signaling



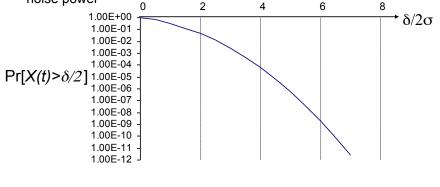
Noise Distribution

- Noise is characterized by probability density of amplitude samples
- Likelihood that certain amplitude occurs
- Thermal electronic noise is inevitable (due to vibrations of electrons)
- Noise distribution is Gaussian (bell-shaped) as below



Probability of Error

- Error occurs if noise value exceeds certain magnitude
- Prob. of large values drops quickly with Gaussian noise
- Target probability of error achieved by designing system so separation between signal levels is appropriate relative to average noise power



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Shannon Channel Capacity

Shannon Channel Capacity:

• The maximum reliable transmission rate over an ideal channel with bandwidth W_c Hz, with Gaussian distributed noise, and with SNR S/N is

$$C = W_c \log_2(1 + S/N)$$
 bits per second

 Reliable means error rate can be made arbitrarily small by proper coding

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Example

- Consider a 3 kHz channel with 8-level signaling.
 Compare bit rate to channel capacity at 20 dB SNR
- 3 kHz telephone channel with 8 level signaling
 Bit rate = 2•3000 pulses/sec 3 bits/pulse = 18 kbps
- 20 dB SNR means $10 \log_{10}(S/N) = 20$ Implies S/N = 100
- Shannon Channel Capacity is then
 C = 3000 log (1 + 100) = 19,963 bits/second

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